

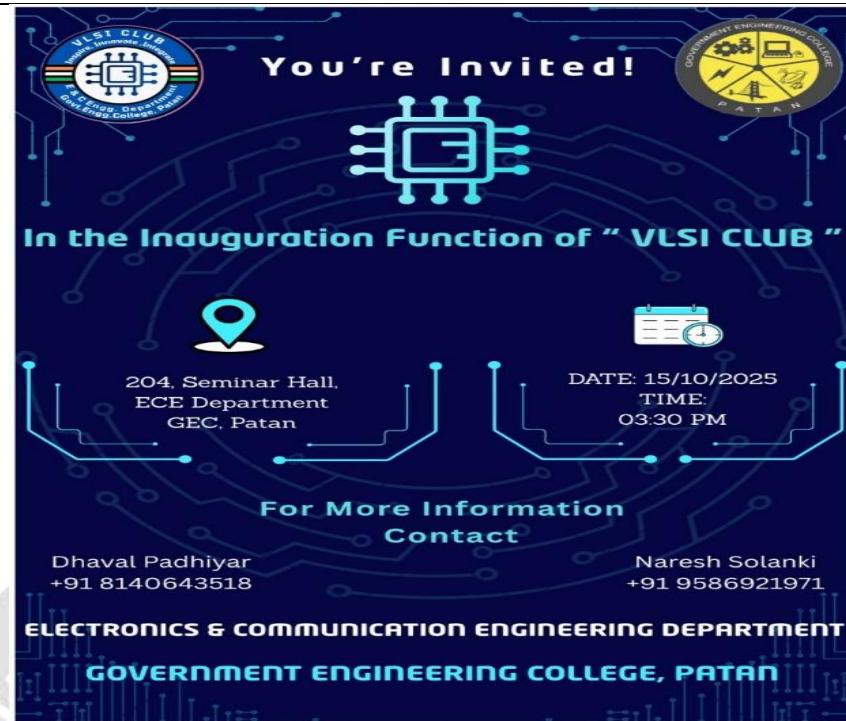
**Government Engineering College, Patan**  
**Electronics & Communication Engineering Department**  
**Event Report**

<b>Title</b>	Inaugural Function of VLSI Club
<b>Date and Time</b>	15/10/2025, 03:30 pm onwards
<b>Mode of Conduct</b>	Offline Mode
<b>Venue</b>	Seminar Room (204), EC Engineering Department, GEC Patan
<b>Expert Details</b>	Not Applicable
<b>Organized By</b>	EC Engineering Department, GEC Patan
<b>Objective</b>	<p>The primary objective of the VLSI Club Opening was to:</p> <ul style="list-style-type: none"> <li>Enhance students' awareness of VLSI design and semiconductor technologies.</li> <li>Provide a platform for industry-oriented learning and skill development.</li> <li>Encourage hands-on learning, innovation, and a culture of research.</li> <li>Bridge the gap between academic knowledge and industry requirements.</li> <li>Motivate students to explore career opportunities in the VLSI and semiconductor sectors.</li> </ul>
<b>Description</b>	<p>The Electronics and Communication Engineering Department of Government Engineering College, Patan, organized the VLSI Club Opening on 15th October 2025. The event commenced with the formal inauguration of the club by Principal Dr. B. J. Shah, Dr. A. B. Dhruv (Professor-Mechanical), and Dr. D. H. Patel (HoD-EC).</p> <p>The VLSI Club has been established to strengthen students' technical competencies in the domain of Very Large-Scale Integration (VLSI). Faculty Coordinator Prof. M. L. Patel addressed the gathering, outlining the VLSI Club's vision, mission, and scope.</p> <p>During the session, the significance of VLSI technology in modern electronics was emphasized, particularly in areas such as semiconductor chip design, embedded systems, Artificial Intelligence hardware, and the Internet of Things (IoT). Proposed activities of the club include:</p> <ul style="list-style-type: none"> <li>Technical workshops</li> <li>Expert lectures</li> <li>Hands-on training with EDA tools</li> <li>Student mini projects.</li> <li>Technical competitions</li> </ul>
<b>Participants</b>	<ul style="list-style-type: none"> <li>Undergraduate students of the Electronics and Communication Engineering Department (Total: 20)</li> <li>Faculty members of the institute (Total: 15)</li> </ul>
<b>Outcomes</b>	<ul style="list-style-type: none"> <li>Formal inauguration of the VLSI Club.</li> <li>Increased awareness of VLSI and semiconductor technologies among students.</li> <li>Enhanced student motivation towards acquiring industry-relevant skills.</li> <li>Strengthening of outcome-based education initiatives.</li> </ul>
<b>Conclusion</b>	The VLSI Club Opening was successfully conducted, aligning with the objectives of outcome-based education and continuous improvement as per NBA and NAAC quality standards.

# Government Engineering College, Patan

## Electronics & Communication Engineering Department

### Poster



### Photographs



**Government Engineering College, Patan**  
**Electronics & Communication Engineering Department**



**Annexures**

1. Approval Letter
2. Expert Invitation Letter
3. Attendance Record
4. Expert Thanks Letter

Prof.M.L.Patel

**VLSI Club Coordinator**

Prof. (Dr.) D. H. Patel

**Head of Department**